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STEVENS DAVIS

JUN 1 6 2005

CERTIFICATE OF Applicant(s): Takeya FU	Docket No. L8462,03113								
Application No. 10/685,387	Filing Date October 16, 2003	Examiner R. Potter		Group Art Unit 2822					
Invention: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF MANUFACTURING THE SAME									
			_						
I hereby certify that this		der 37 CFR 1.111 w/ Amendm (Identify type of correspondence)							
is being facsimile transf	mitted to the United States Paten	t and Trademark Office (Fax.	No.	703-872-9306					
on June 16									
James E. Ledbetter, Reg. No. 28,732 Typed or Printed Name of Person Signing Cortificate) (Signature)									
Note: Each paper must have its own certificate of mailing.									
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AMENDMENT TRANSMITTAL LETTER (Large Entity) Applicant(s): Takeys FUJINO et al.					Docket No. L8462.03113			
Application No.	Filing Date October 16, 2003	Examiner R. Potter	Customer N	o. Group Art Unit	Confirmation No. 9778			
Invention: SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF MANUFACTURING THE SAME								
	COMMISSIONER FOR PATENTS:							
	Transmitted herewith is an amendment in the above-identified application. The fee has been calculated and is transmitted as shown below.							
		CLAIMS AS AM	ENDED					
	CLAIMS REMAINING	HIGHEST # PREV. PAID FOR	NUMBER EXTRA	RATE	ADDITIONAL FEE			
TOTAL CLAIMS	30 -	30 =	0	x \$50.00	\$0.00			
INDEP. CLAIMS	13 •	13 = .	0	× \$200.00	\$0.00			
Multiple Depender	Multiple Dependent Claims (check if applicable)							
No additional fee is required for amendment. ☐ Please charge Deposit Account No. in the amount of ☐ A check in the amount of to cover the filing fee is enclosed. ☐ The Director is hereby authorized to charge payment of the following fees associated with this communication or credit any overpayment to Deposit Account No. ☐ Any additional filing fees required under 37 C.F.R. 1.16. ☐ Any patent application processing fees under 37 CFR 1.17. Dated: June 16, 2005								
James E. Ledbetter Reg. No. 28,732 Certify that this document and fee is being deposited of with the U.S. Postal Service as first class mall under 37 C.F.R. 1.8 and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450. Signature of Person Mailing Correspondence Typed or Printed Name of Person								

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Inventor: Takeya FUJINO et al. Group Art Unit: 2822

Appln. No.: 10/685,387

Examiner: R. Potter

Filed:

October 16, 2003

For:

SEMICONDUCTOR INTEGRATED CIRCUIT AND METHOD OF

MANUFACTURING THE SAME

AMENDMENT UNDER 37 CFR § 1.111

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Office Action dated March 16, 2005, please amend the above-captioned application as follows: